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AMENDMENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Frank Bryant, et al.

Art Unit: 2822

Examiner: Lewis, M.

Serial Number: 09/813,087

10 Filed: March 19, 2001

Title: PRINTHEAD INTEGRATED CIRCUIT

Date: December 6, 2002

TECHNOLOGY CENTER 2800

DEC 13 2002

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AMENDMENT UNDER 37 CFR § 1.111

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TO THE ASSISTANT COMMISSIONER FOR PATENTS:

Sir:

20 In response to the Office Action dated August 27, 2002, please amend the
above-identified patent application as follows:

In the Specification:

Please replace the paragraph starting on page 4, line 30 with the following:

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Fig. 1 is an exemplary cross-section of a conventional integrated circuit 11
that combines a transistor and ejection element. A substrate 10, preferably
silicon though other substrates known to those skilled in the art can be used and
still meet the spirit and scope of the invention, is processed using conventional
30 integrated circuit processes. The substrate 10 is preferably doped with a p-
dopant for an NMOS process; however, it can also be doped with an n- dopant